

DD/A Registry  
85-2756

## ROUTING AND RECORD SHEET

SUBJECT: (Optional)			
FROM:		EXTENSION	NO.
D/OIT 2D00 Headquarters			
TO: (Officer designation, room number, and building) <i>EO-500 8/11</i>		DATE	
		RECEIVED	FORWARDED
		OFFICER'S INITIALS	
COMMENTS (Number each comment to show from whom to whom. Draw a line across column after each comment.)			
<p>1. A/DDA 7D18 Headquarters</p> <p>2.</p> <p>3.</p> <p>4.</p> <p>5.</p> <p>6.</p> <p>7.</p> <p>8.</p> <p>9.</p> <p>10.</p> <p>11.</p> <p>12.</p> <p>13.</p> <p>14.</p> <p>15.</p>			
<p>STAT</p> <p>Jim,</p> <p>Please note paragraph 3 of the attached memo.</p> <p>I strongly urge that OIT be given the 2000 sq. ft. which is not TSOC space. It appears that this might save \$250,000.00, but perhaps more importantly, it would probably assure a successful transition.</p> <p>I spoke to [redacted] about this and he said, "When you have two billion dollars up there in space I don't want to be off the air if I don't have to be. I think the space should be obtained."</p> <p>We don't want outages in this program. Can we have that 2000 sq ft?</p> <p>STAT</p> <p>50-1</p>			

5 AUGUST 1985

MEMORANDUM FOR: See Distribution

VIA: Chief Design and Development Branch

FROM: [redacted] Design and Development Branch

STAT

SUBJECT: A Revised CAMSII Upgrade Plan

1. The purpose of this memorandum is to provide you with an update of the Processing Systems Group's plans for the CAMSII hardware upgrade. This update is presented at this time because of the concern of CAMSII having either no Backup Processor or no Processor for extended periods of time during the hardware upgrade scheduled for FY-86. The FY-86 Plan is to provide CAMSII with an additional Processor at both the Production Site (1D16) and Development Site (TRW's building W2). The following points describe, from PSG's perspective, the steps required to implement the FY-86 hardware and the alternatives available to improve the situation.

- a. The current CAMSII Production Processor (3084 Q) must be split or separated into two functional 3081 Model K's, at a cost of \$250,000.00 dollars. This will require a total outage of 12 hours for the Production Application.
- b. In order to accommodate the New Processor, a IBM Sierra 3090, some of the existing CAMSII disk drives must be relocated in 1D16. This will require 8 hours of down time for the CAMSII Application, however this activity is scheduled to occur when the 3084 Model Q is split to minimize outages.
- c. Due to space limitations, the Production Backup Processor must be de-installed and removed before the installation of the New Processor can begin. The installation and testing of the New Processor will require 5 days, the New Processor will be used initially as the Backup Processor.
- d. The Production System uses the Intelligent Data Base Machine (IDM) which has not been tested with the 3090 processor. This is a significant risk that could extend the installation period and delay production status for the new 3090 processor.

2. In order to implement the FY-86 CAMSII Hardware Plan outlined in the previous paragraph, the following schedule is proposed:

- a. Split the Production 3084 Model Q on 26 October 1985.
- b. Install the New 3090 Processor on 22 November 1985.
- c. IPL (run) 3090 Processor as Production Backup 27 November 1985.
- d. IPL (run) 3090 Processor as Production CAMSII 4 December 1985.

e. Relocate the separated 3081 Model K to TRW W2 site 6 December 1985.

f. Install the 3081 Model K at W2 on 9 December 1985.

3. An alternative to the above plan is to acquire the TSOC space adjacent to 1D16 [redacted] Annex). This additional 2000 square feet would provide the necessary space to insure the 3090 installation and testing of IDM equipment independent of the CAMSII Production Processors. In Addition, it may eliminate the need of splitting the 3084 Model Q which would save \$250,000.00 dollars. Although this would require an adjustment to the schedule to accommodate the renovation of the TSOC space it is the only way we can assure a successful transition to the New Processor.

STAT

[redacted]  
OIT/PSG/CSED/DDB

STAT